

Patent



Attorney Docket No.: Intel 2207/10122
Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Sailesh B. KOTTAPALLI
SERIAL NO. : 09/753,764
FILED : December 29, 2000
FOR : METHOD AND APPARATUS FOR INSTRUCTION
POINTER STORAGE ELEMENT CONFIGURATION IN
A SIMULTANEOUS MULTITHREADED PROCESSOR
GROUP ART UNIT : 2183
EXAMINER : David J. HUISMAN

M/S: ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: M/S: ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on	
Dated: November 11, 2005	Signature: <u>Barbara Vance</u> Barbara Vance

AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. §1.312

Dear Sir:

Applicant respectfully requests that the above-referenced application be amended after allowance, but prior to payment of the issue fee.

Listing of Claims begin on page 2 of this paper.

Amendments to the Drawings begin on page 9 of this paper and include both annotated sheets and replacement sheets.

Remarks/Arguments begin on page 10 of this paper.

An **Appendix** including amended drawings and replacement drawings are attached after page 10 of this paper.